

REMARKS**I. Introduction**

These amendments and remarks are being filed in response to the Office Action dated November 16, 2007. Claims 1-13, 15, 18 and 19 are pending. Claims 14, 16 and 17 have been cancelled by this paper. Claims 5, 6, 9-11 18 and 19 were withdrawn by the Examiner following a Restriction Requirement. Claim 12 was allowed. Claim 17 was objected to as being allowable if re-written in independent form. Applicants amended independent claim to include the elements of dependent claim 17 and intervening claim 16.

Claim 1 has been amended and is supported by the originally filed specification at page 7, line 24 to page 8 line 4 and also by original claim 14, which has been cancelled.

Claim 13 has been amended to include all of the elements of objected to as allowable claim 17 and intervening claim 16.

Claims 2 and 7 have been rewritten in independent form. Claim 4 has been amended and is supported by the originally filed specification at page 7, line 24 to page 8, line 4.

As such, no new matter has been added by this amendment. If this application is not allowed, Applicants submit this amendment should be entered upon filing an appeal, as it reduces the issues for appeal.

For the following reasons, the application should be allowed and the case passed to issue.

II. Claim Rejections – 35 U.S.C. § 112, first paragraph

Claims 1-4, 7, 8 and 14 were rejected under 35 U.S.C. § 112, first paragraph as allegedly failing to comply with the written description requirement. Specifically, the Examiner contends that there is no description in the specification for the claim term “as it becomes successively

saturated with information,” as was recited in independent claim 1. Applicants respectfully submit that claim 1 has been amended to delete the above claim term, and now recites,

A memory matrix device for storing temporally sequential information in a manner that retains the sequence of information without dependence on multiple memory addresses, and is not a serial sequential access memory, a random access memory or a dynamic random access memory, comprising:

sequentially-connected arrays of fixed memory storage units;
means for applying the temporally sequential information to the arrays of fixed memory storage units; and

means for successively latching and disabling each successive fixed memory storage unit in a sequentially-connected array of said units, each array becoming enabled and then unenabled in temporal sequence, thereby directing the next temporal bit of information to the next memory storage unit in said sequentially-connected array, and

wherein, the input to the sequentially-connected array of fixed memory storage units is disabled upon completion of storage of a temporally sequential event to prevent overwriting.

The amendment to claim 1 is clearly described throughout the specification, for example at page 7, line 24 to page 8 line 4 and claim cancelled claim 14.

Claim 1 requires that the memory matrix device successively latches and disables each successive fixed memory storage unit in a sequentially-connected array of the units, each array becoming enabled and then unenabled in temporal sequence, thereby directing the next temporal bit of information to the next memory storage unit in the sequentially-connected array.

As such, it is respectfully submitted that the amendment to claim 1 obviates the rejection under 35 U.S.C. § 112, first paragraph.

Furthermore, claims 2-4, 7 and 8 were rejected for depending on claim 1 and therefore, it is respectfully submitted that the amendment to claim 1 obviates the rejection under 35 U.S.C. § 112, first paragraph as applied to these claims.

II. Claim Rejections – 35 U.S.C. § 103(a)

Claims 1, 3, 13, 15 and 16 were rejected under 35 U.S.C. § 103(a) as allegedly being unpatentable over Wu U.S. 6,845,436. Applicants respectfully disagree. However, in order to expedite prosecution, independent claims 1 and 13 have been amended.

Claim 1 has been amended to incorporate elements of dependent claim 14 which was not rejected over prior art.

Claim 1 includes the terms of claim 14, which did not receive a prior art rejection, “wherein, the input to the sequentially-connected array of fixed memory storage units is disabled upon completion of storage of a temporally sequential event.”

Accordingly, it is respectfully submitted that claim 1 is allowable.

Furthermore, claims 3 and 4 depend from and further define the subject matter of claim 1 and therefore are also allowable.

As discussed above, claim 13 has been amended to include all of the elements of objected to as allowable claim 17 and intervening claim 16.

Accordingly, it is respectfully submitted that claim 13 is allowable.

Furthermore, claim 15 depends from and further define the subject matter of claim 13 and therefore is also allowable.

III. Claims 2 and 7

Claims 2 and 7 were not rejected over any prior art and have been re-written in independent form.

Accordingly, it is respectfully submitted that claims 2 and 7 are allowable.

Furthermore, claim 8 depends from claim 7 and further defines the subject matter of the claim and therefore is also allowable.

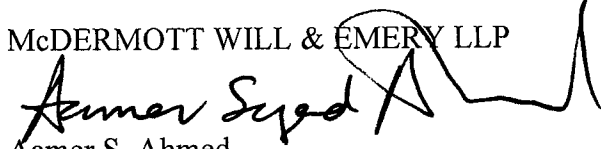
IV. Conclusion

In view of the above amendments and remarks, Applicants respectfully submit that this application should be allowed and the case passed to issue. If there are any questions regarding this Amendment or the application in general, a telephone call to the undersigned would be appreciated to expedite the prosecution of the application.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP


Aamer S. Ahmed
Registration No. 58,958

600 13th Street, N.W.
Washington, DC 20005-3096
Phone: 202.756.8000 ASA:MWE
Facsimile: 202.756.8087
Date: February 14, 2008

**Please recognize our Customer No. 20277
as our correspondence address.**